

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09605293
Filing Date June 28, 2000
Confirmation No. 5927
Inventor Chapek, David L.
Assignee Micron Technology, Inc.
Group Art Unit 2815
Examiner Kim, Jay C.
Attorney's Docket No. MI22-4201
Title: Semiconductor Devices Including a Layer of Polycrystalline Silicon Having a Smooth Morphology

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References - - See attached Form PTO/SB/08A-B

The attached Form PTO/SB/08 is submitted in compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, and your attention is directed to the references listed on the attached Form PTO/SB/08. No copies of any cited U.S. patents or U.S. published applications are included herewith. Copies of all other cited art are attached. No admission is made regarding whether all the submitted references are prior art.

The fee of \$180.00 is being charged to the credit card information provided electronically.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 7-26-10

By: 
Mark S. Matkin
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